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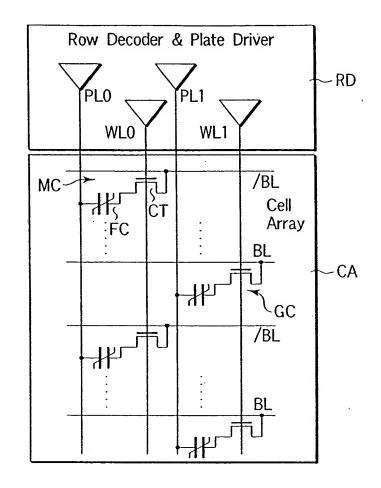


FIG. 1 PRIOR ART

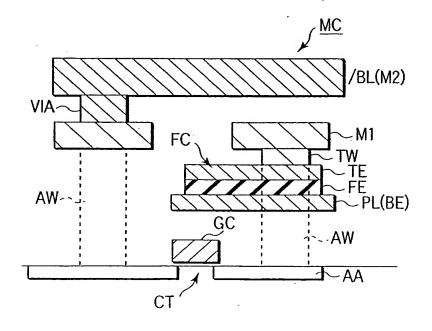


FIG. 2 PRIOR ART

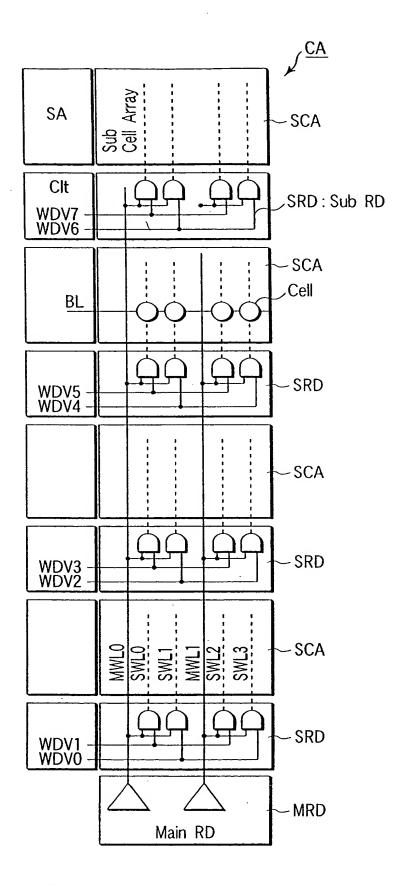


FIG. 3 PRIOR ART

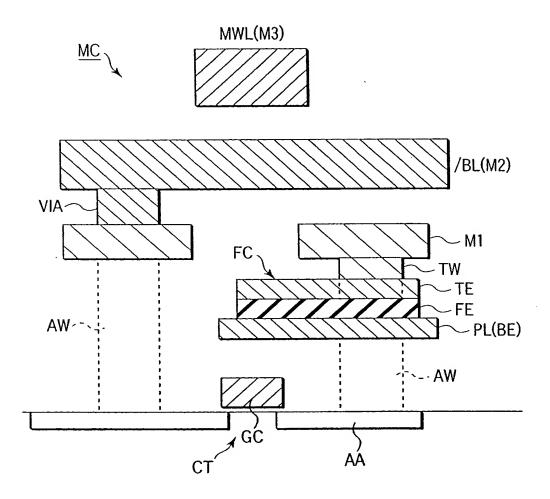


FIG. 4 PRIOR ART

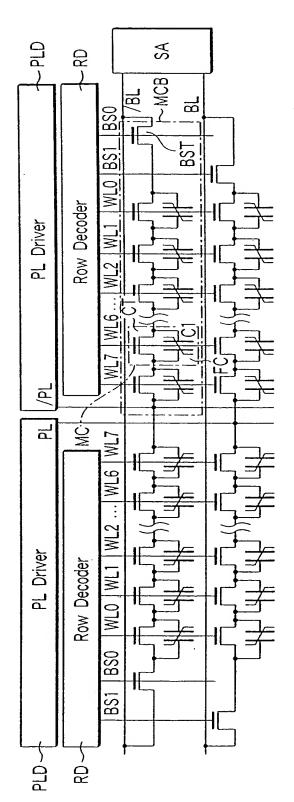
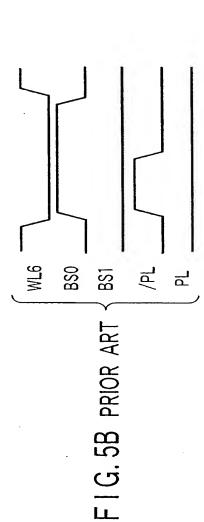


FIG. 5A PRIOR ART



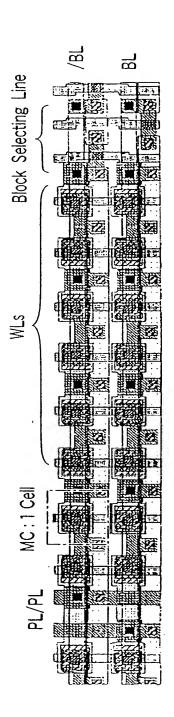


FIG. 6 PRIOR ART

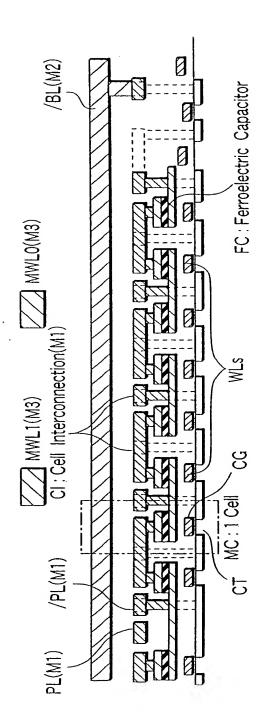
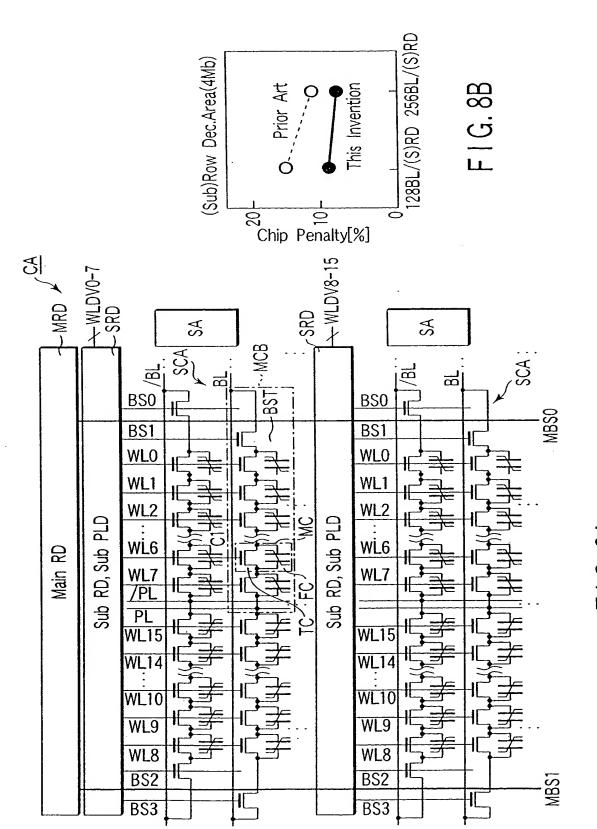


FIG. 7 PRIOR ART



F I G. 8A

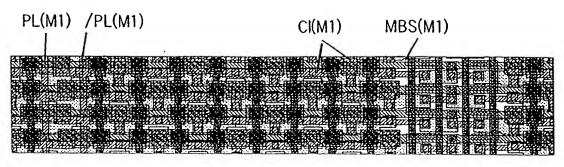
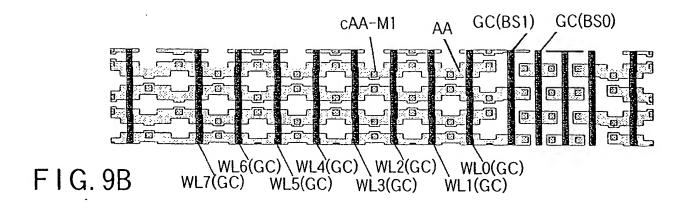
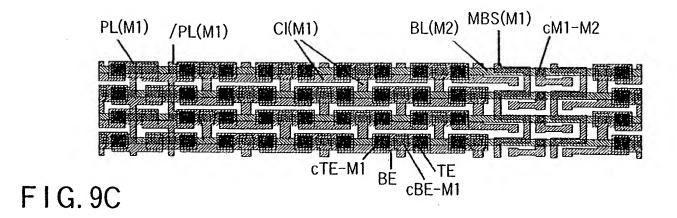
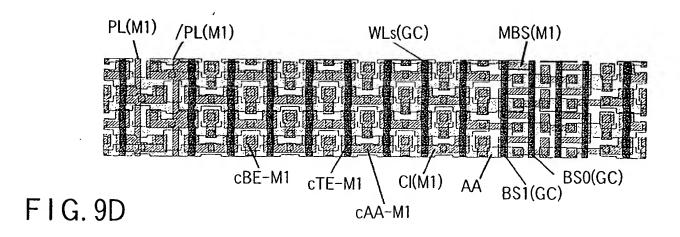


FIG.9A







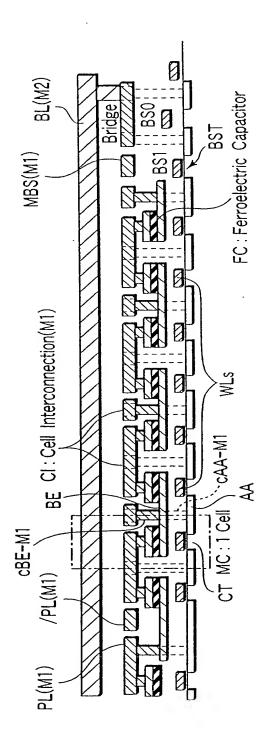
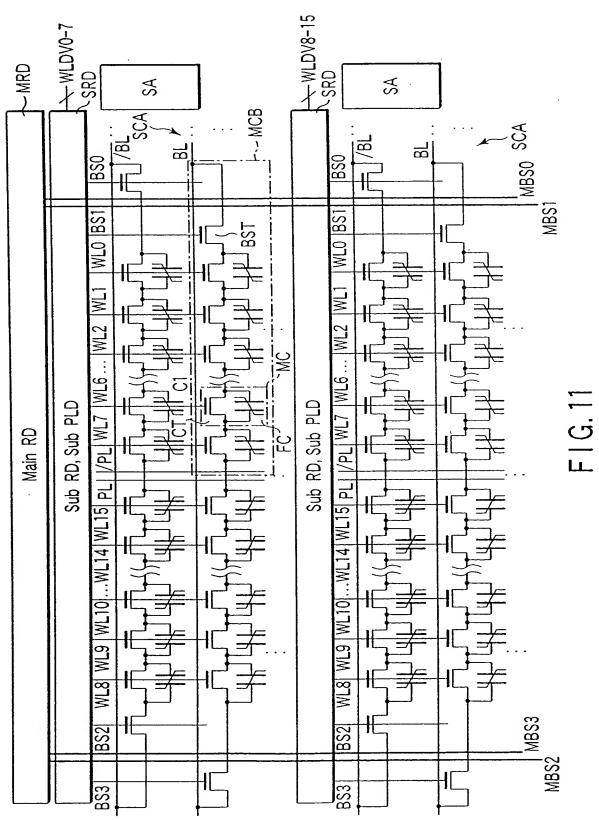


FIG. 10



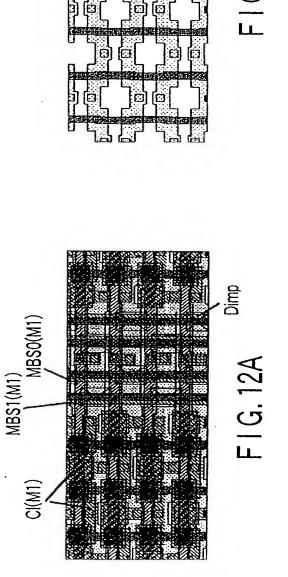


FIG. 12B

FIG. 12B

FIG. 12B

CI(M1)

MBS1(M1) MBS0(M1)

CTE-M1

TE

CBE-M1

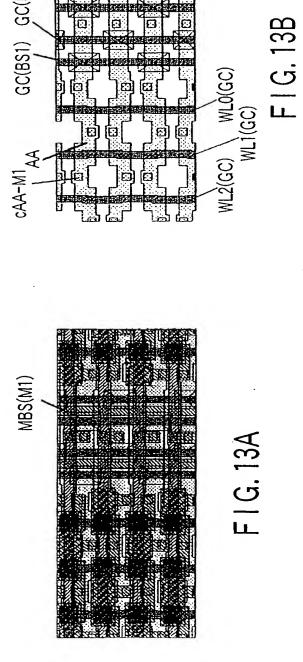
Dimp

CBE-M1

MBS1(M1) MBS0(M1) BL(M2) / cM1-M2

^{-- свè-м1} F I G. 12С

CTE-M1



GC(BS1) GC(BS0)

F1G. 13A

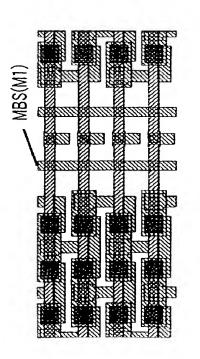


FIG. 13C

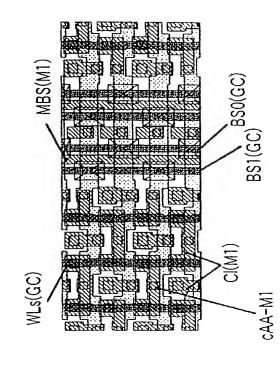
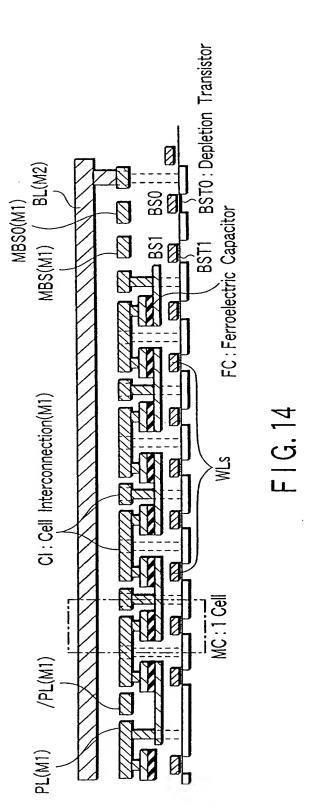
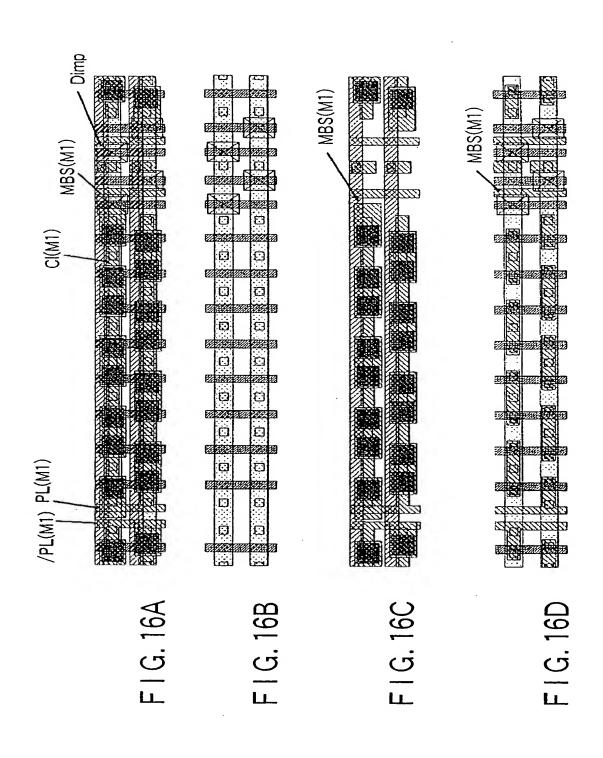


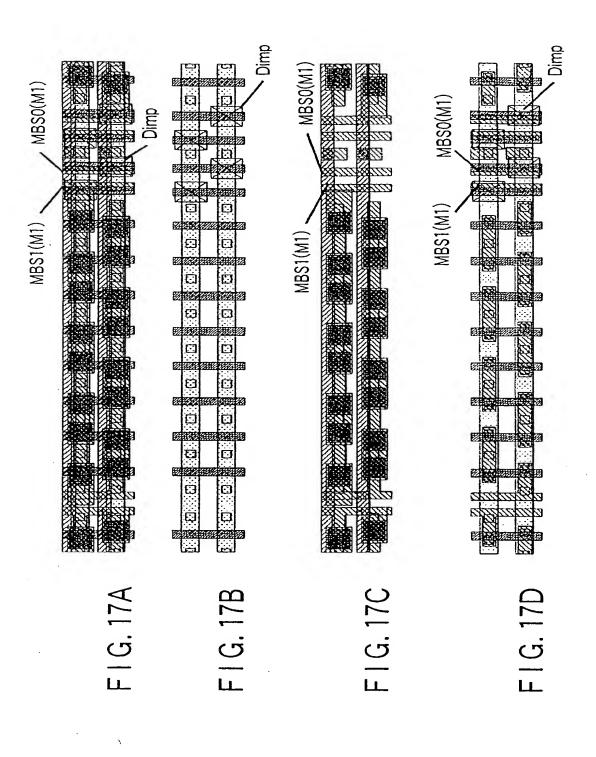
FIG. 13D

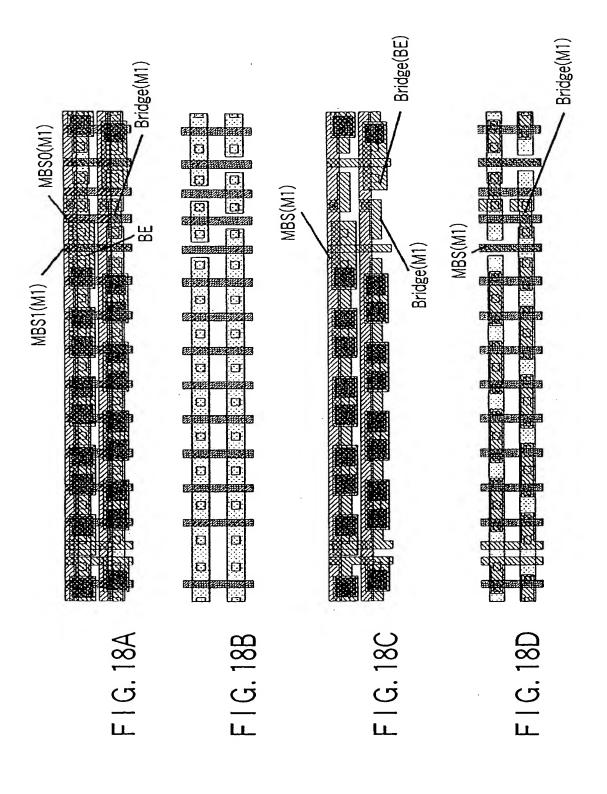


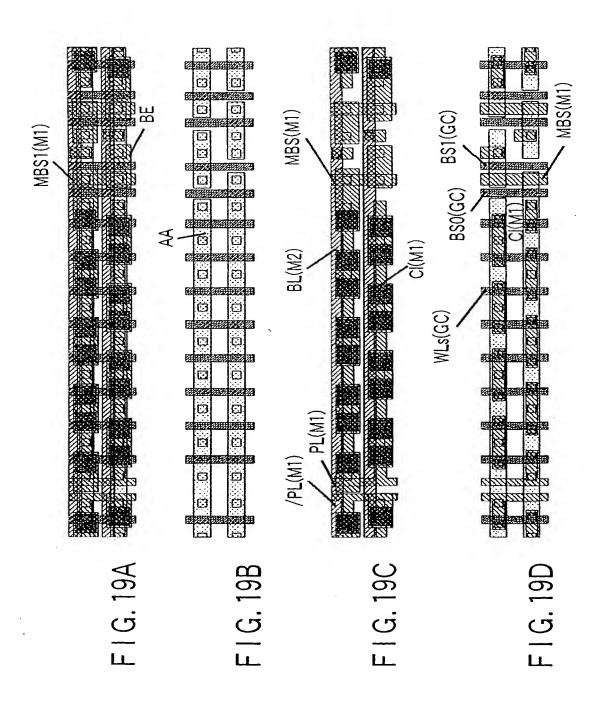
BST0: Depletion Transistor MBS(M1) BL(M2) 880 FC: Ferroelectric Capacitor CI: Cell Interconnection(M1) /PL(M1)

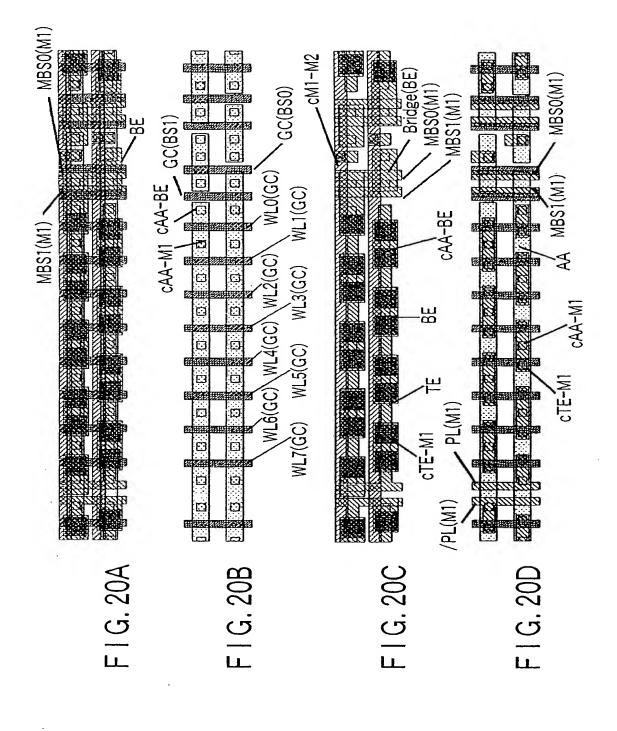
F1G. 15

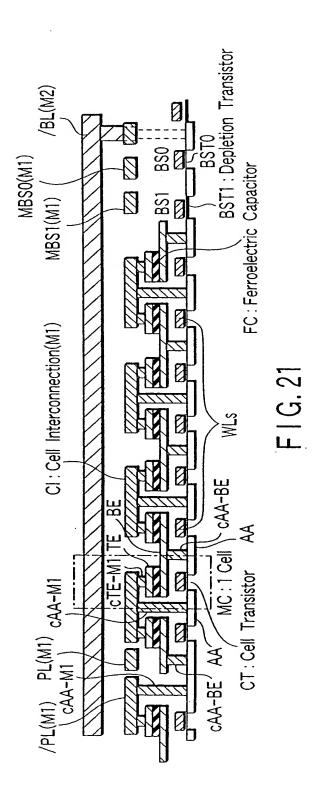












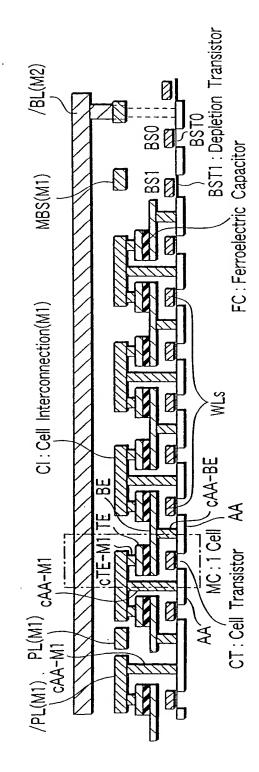
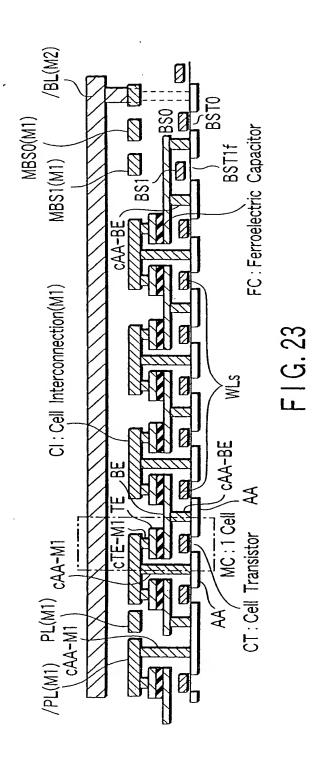
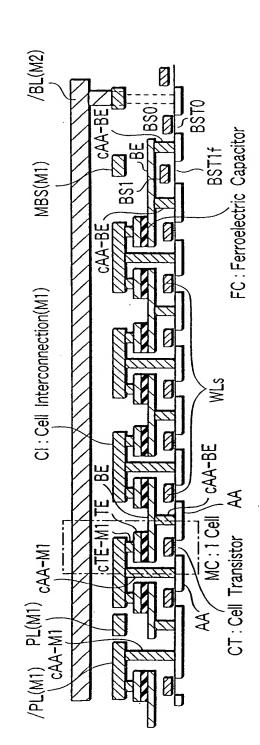
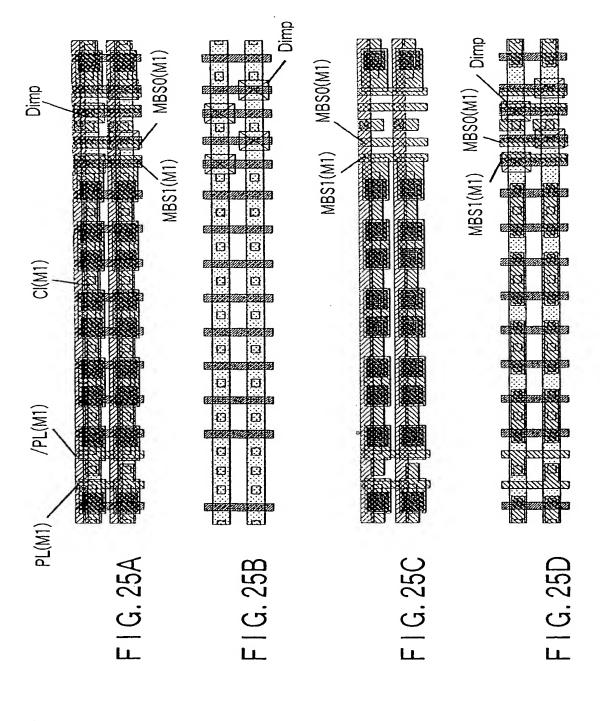


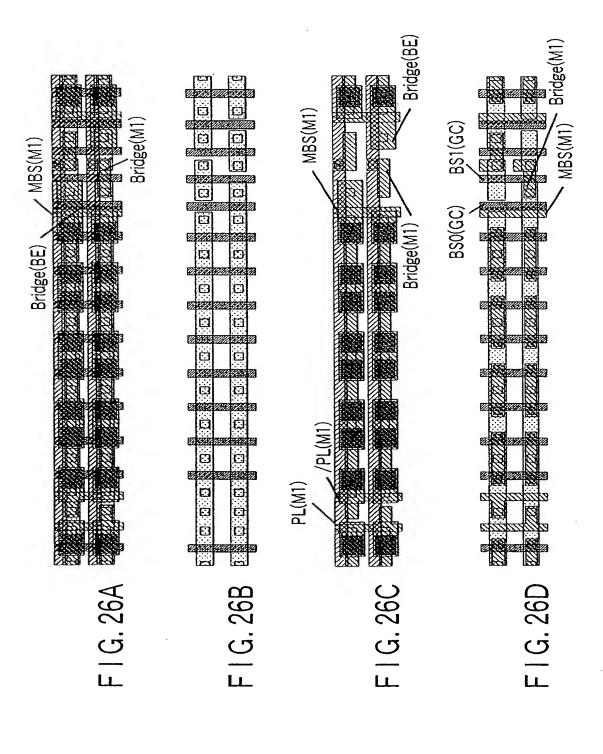
FIG. 22

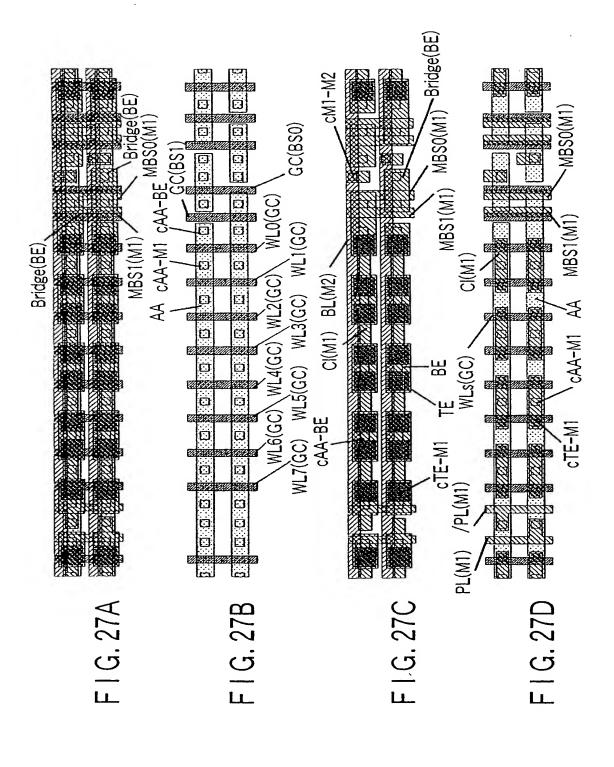


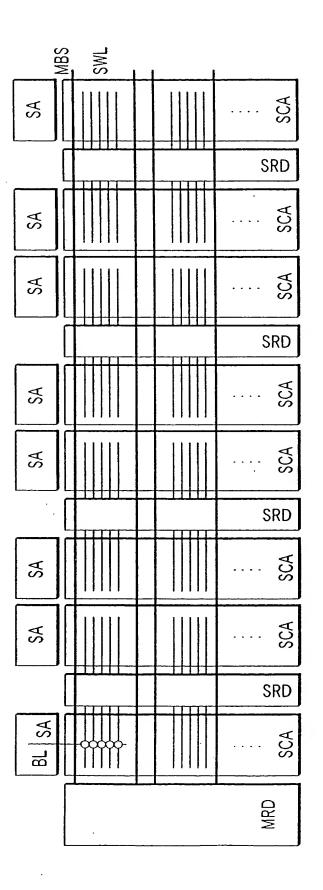


F1G.24

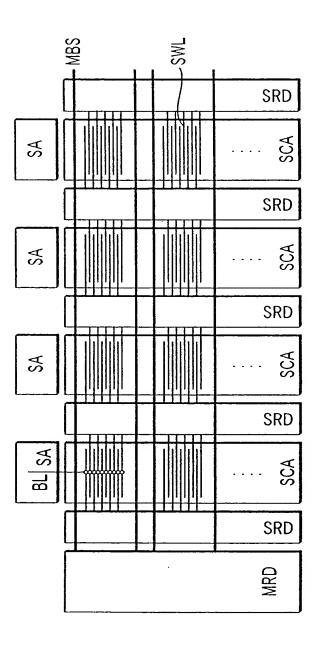








F G. 28



F1G. 29

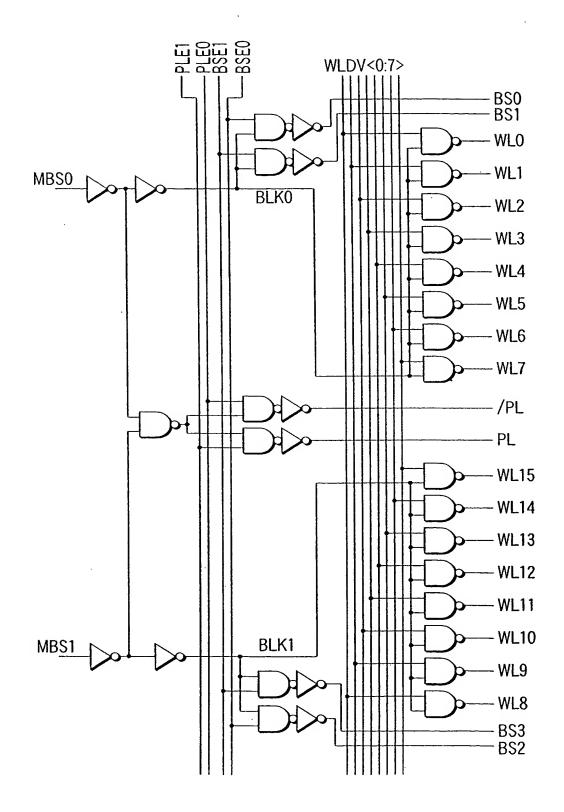
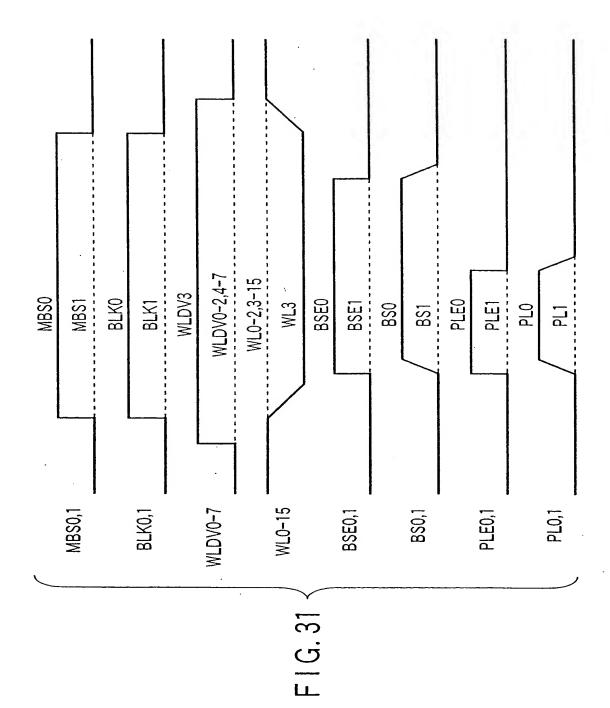


FIG. 30



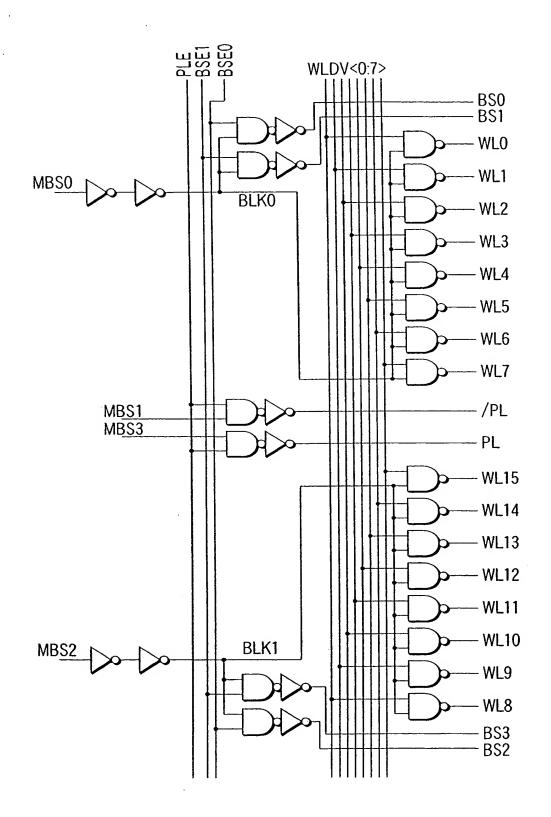


FIG. 32

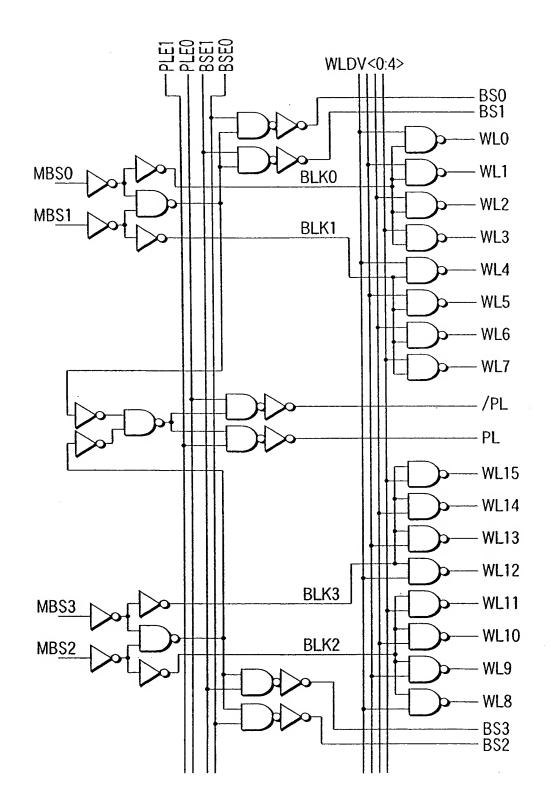


FIG. 33

